

Self-rolled-up ultrathin single-crystalline silicon nanomembranes for on-chip tubular polarization photodetectors

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Freestanding single-crystalline nanomembranes and their assembly have broad application potential in photodetectors for integrated chips. However, the release and self-assembly process of single-crystalline semiconductor nanomembranes still remains great challenges in on-chip processing and functional integration, and photodetectors based on nanomembrane always suffer from limited absorption of nanoscale thickness. Here, we employ a non-destructive releasing and rolling process to prepare tubular photodetectors based on freestanding single-crystalline Si nanomembranes. Spontaneous release and self-assembly are achieved by residual strain introduced by lattice mismatch at the epitaxial interface of Si and Ge, and the intrinsic stress and strain distributions in self-rolled-up Si nanomembranes are analyzed experimentally and computationally. The advantages of light trapping and wide-angle optical coupling are realized by tubular geometry. Our Si microtube device achieves reliable Ohmic contact and exhibits a photoresponsivity of over 330 mA/W, a response time of 370 μ s, and a light incident detection angle range of over 120°. Furthermore, the microtubular structure shows a distinct polarization angle-dependent light absorption, with a dichroic ratio of 1.24 achieved at 940 nm. The proposed Si-based microtubes provide new possibilities for the construction of multifunctional chips for integrated circuit ecosystems in More than Moore era.

1. Introduction

Freestanding semiconductor nanomembranes with high crystallinity exhibit excellent photonic and optoelectronic properties.^[1-3] Tuning the thickness of freestanding nanomembranes usually only affects their mechanical properties,^[4,5] such as Young's modulus and stiffness, without affecting its bandgap. This characteristic is advantageous for the manufacture of flexible devices with 3D geometry.^[6,7] 3D self-assembled freestanding nanomembranes offer a wider range of optical functions and mechanical structural designs than 2D planar materials.^[8-10] Typically, the fabrication process of freestanding 3D single-crystalline nanomembranes relies on heterogeneous epitaxial techniques with lattice interfaces and artificial termination first,^[11-14] which is often considered as

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lattice matching. During epitaxial processes, residual strain will be inevitably introduced into epitaxial single-crystalline nanomembranes via lattice mismatch, which often results in a short lifetime and poor uniformity of planar devices.^[15-17] Nevertheless, proper use of residual strain can also yield additional benefits. For example, in order to support the development of Moore's Law, internal stress and strain are introduced into Si and Ge to adjust the energy band structure and improve carrier mobility.^[18-20] The epitaxial nanomembranes are then separated from the substrate by layer separation techniques (e.g., chemical lift-off, 2D-assisted lift-off, mechanical spalling).^[1,4,21,22] The released single-crystalline nanomembranes can then be assembled to fabricate 3D electronic devices with targeted functions, such as high-performance lasers,^[23] memories,^[11] and photodetectors.^[1]

Rolled-up nanomembranes,^[6,8,24] as a fast-evolving technology, can be combined with existing chip manufacturing methods to convert specified 2D precursor structures into micro/nano tubular devices with 3D geometries,^[7,10] which can offer a promising chip integration platform by forming 3D volumetric devices with a more compact aerial footprint. Therefore, self-rolled-up technology can be used as a new integration technology to roll up centimeter-scale nanomembranes into micro-scale microtubes, which has been effectively utilized to fabricate devices such as field-effect transistor,^[25] heterostructure,^[9] and memory.^[26] In addition, rolled-up nanomembrane can introduce stress to adjust the band gap and carrier transport of the semiconductor,^[27,28] and rolled-up nanomembrane microtube can also act as whispering-gallery optical microcavity or optical trap.^[29,1] Hence, the integration of rolled-up nanomembrane technology with Si, the foremost semiconductor material in today's integrated circuit industry, offers significant advantages and is a promising approach to electronic and optoelectronic technological advancement. Currently, epitaxy Si nanomembranes on Si, Ge, and $\text{Si}_{1-x}\text{Ge}_x$ substrates are highly advanced technologies that have been extensively utilized in state-of-the-art integrated circuit manufacturing.^[30-32] Typically, the epitaxial interface between Si and Ge can induce approximately 4.18% interfacial strain.^[33-35] When the epitaxial single-crystalline Si nanomembrane on Ge or $\text{Si}_{1-x}\text{Ge}_x$ sacrificial layer is released, residual stress at the epitaxial interface can spontaneously drive the formation of 3D structures, including tubular and wrinkled structures,^[7,36,37] which is a solid foundation for the preparation of 3D Si nanomembranes electronic and optoelectronic devices.

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In this article, we used etchant to selectively etch Ge sacrificial layer and to release single-crystalline Si nanomembranes. The released Si nanomembranes rolled up into microtubes. The high-angle annular dark-field scanning transmission electron microscopy (HAADF-STEM) image shows that the self-rolled-up Si nanomembranes preserve an excellent single-crystalline structure. Through the analysis of microtubes with varying thicknesses, the relationship between the diameter of microtubes and the thickness of Si nanomembranes is obtained, and the strain distribution of Si nanomembranes after self-rolled-up is calculated. Based on the analysis, the electrical transport mechanism and photoresponse in visible/near-infrared spectroscopy (VIS-NIRs) of Si microtubes are studied. Remarkably, the Si microtubular photodetector not only demonstrates wide-angle incident light detection, but also suggests promising potential for future polarized light detection.

2. Results and Discussion

2.1 Fabrication and Characterization of Single-Crystalline Si microtubes

Epitaxy of Si nanomembrane on Ge is a well-developed technology in the field of epitaxial semiconductor materials. **Figure 1a** shows a cross-sectional HAADF-STEM image near the Si/Ge epitaxial interface. Single-crystalline Si nanomembranes are epitaxially grown on Ge (001) by molecular beam epitaxy (MBE). Ge serves as a sacrificial layer that can be dissolved in the hydrogen peroxide (H_2O_2) solution. The right side of Figure 1a shows the fast Fourier transform (FFT) pattern of the epitaxial Si/Ge interface, revealing excellent single-crystalline quality and hetero-epitaxial interface. The two separate diffraction spots located at both (004) and (0 $\bar{4}$ 0) correspond to the interplanar spacing of Si and Ge, respectively. The SEAD patterns of Si and Ge without epitaxial interfaces are shown in Figure S1. The red spots represent Si with a (004) and (0 $\bar{4}$ 0) interplanar spacing of ~ 0.143 nm, and the orange spots correspond to Ge with a (004) and (0 $\bar{4}$ 0) interplanar spacing of ~ 0.150 nm. The mismatches of the (220) crystal plane of Si and Ge at epitaxial interface are analyzed in Figure S2a. Lattice mismatch of Si and Ge results in tensile strain on the single-crystalline Si nanomembrane at the epitaxial interface, and correspondingly, introduces compressive strain on Ge at the epitaxial interface. We calculated strain maps of ε_{xx} , which range from -5% to +5% by using geometrical phase analysis at atomic scale,^[11,12] as shown in Figure 1b and Figure S2b.

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We fabricated the Si/Ge bilayer into an array of squares with a fixed end by photolithography and reaction ion etching, as shown in Figure 1c. The Si layer was immersed in the H_2O_2 solution at 70°C for several minutes until the Si nanomembranes were separated from the substrate and spontaneously rolled-up (Figure S3). Figure 1d shows a self-rolled-up microtube array of Si nanomembrane with a thickness of 30 nm. Similarly, self-rolled-up microtubes of Si nanomembranes with a thickness of 20 nm are shown in Figure S4. The self-rolled-up behaviors are due to the internal strain caused by the different lattice constants between Si and Ge. During epitaxial process, tensile and compressive strains were introduced by lattice mismatch between different layers. Specifically, the lattice constants of Si and Ge, both of which also have diamond structures, are 0.543 and 0.566 nm,^[38,39] respectively. The tensile strain in epitaxial Si nanomembranes is confirmed by wavelength-dependent Raman spectroscopy, as shown in Figure S5. During epitaxial Si nanomembrane release process, the lower surface experiences a larger tensile strain compared to the upper surface, leading to a downward self-rolled-up of the released Si nanomembrane. In order to strengthen the sample, an 80-nm-thick aluminum oxide (Al_2O_3) layer was coated on the self-rolled-up Si microtubes via atomic layer deposition (ALD) to stabilize the tubular structure and protect the crystal structure from destruction during focused ion beam (FIB) cutting and sample preparation. By removing an arched sample from the Si microtube wall with FIB, the tightly bonded self-rolled-up Si nanomembranes between the Al_2O_3 layers were observed, as shown in Figure 1e and Figure S6. The Si layer below Al_2O_3 is formed by ion bombardment of Si and sputtering on the inner wall of the microtube during the FIB sampling process. Figure 1f shows an enlarged TEM image of a small segment of the Si microtube wall. The inner side of each tube wall is the epitaxial interface of Si and Ge, which is significantly flatter than the exposed surface on the outside. Impurity particles between layers are likely to be introduced after wet etching and critical point drying. Figure 1g shows an atomic scale HAADF-STEM image of the Si microtube, demonstrating that the self-rolled-up Si nanomembrane maintain a complete single-crystalline structure after release. The corresponding FFT pattern is shown in Figure 1h. In addition, our self-rolled-up Si nanomembrane microtubes are tightly rolled and stuck layer by layer, with a gap of about 3-4 nm. Moreover, the gap in the most compact region is as short as 1 nm, and a clear atomic structure can also be observed (Figure S7). Typically, the rolled-up direction of the isotropic nanomembrane is mainly modulated by the pre-strain and the predetermined pattern.^[40,41] However, the self-rolled-up direction of epitaxial nanomembranes is

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limited by the crystal orientation.^[6,42] By polarization Raman characterization,^[43,44] we found that the angle between the rolling direction of microtubes and the lattice direction is about 9.90° (Figure S8 and Figure S9), showing excellent geometrical control.

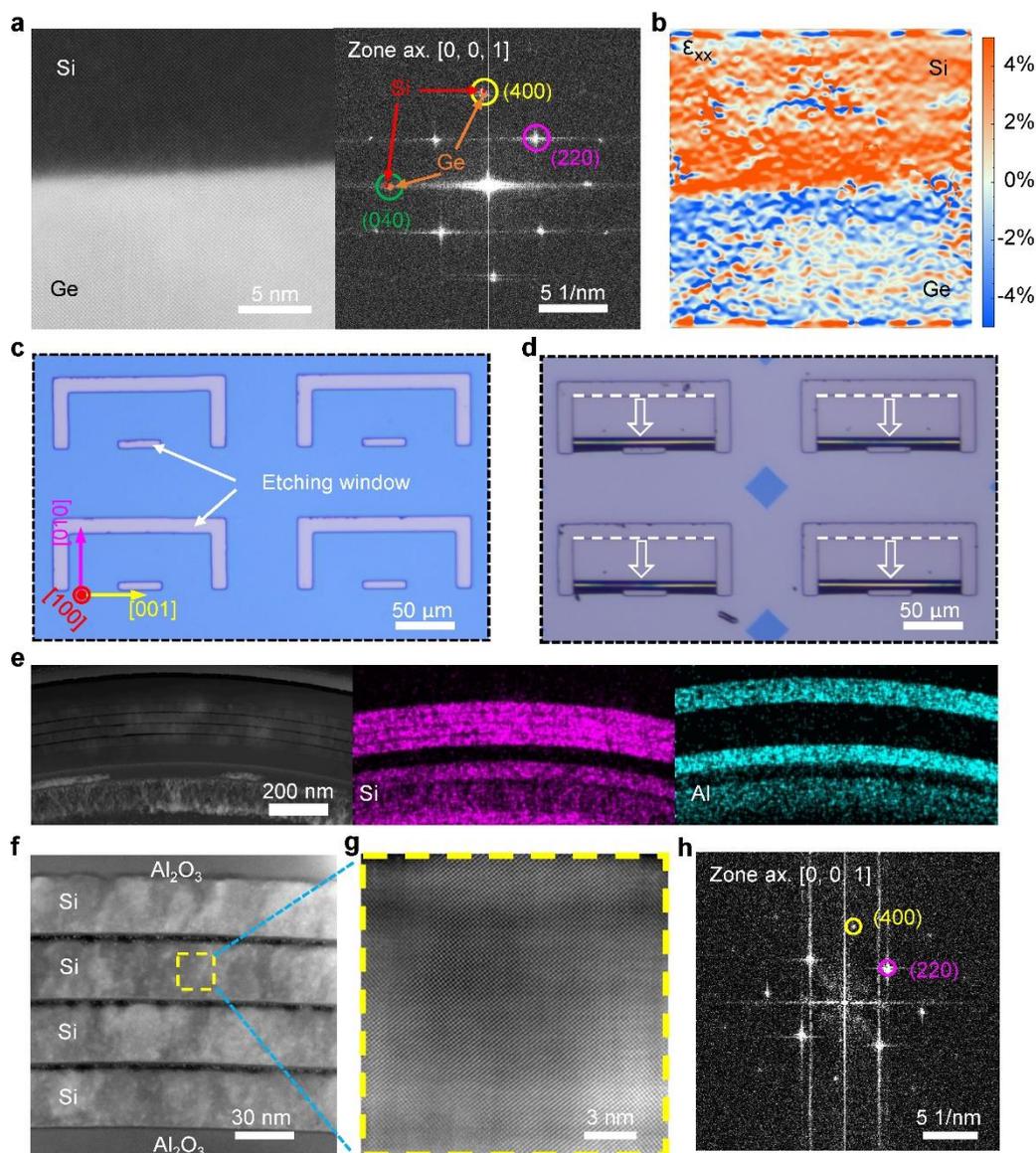


Figure 1. Fabrication of self-rolled-up single crystalline Si microtube. a) Cross-sectional HAADF-STEM image of the interface of epitaxial Si grown on Ge (left panel), viewed along [001] zone axis and corresponding FFT pattern (right panel). b) Geometric phase analysis image of in-plane strain distribution near the Si/Ge interface corresponding to panel a. c) Optical image of the flat and pre-

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patterned Si nanomembranes with etching windows. d) Optical image of self-rolled-up Si microtubes. e) Low-magnification cross-sectional TEM image of the self-rolled-up Si microtube wall and the corresponding chemical element distribution. f) A zoomed-in STEM image of the self-rolled-up Si multilayer. g) Atomic-resolution HAADF-STEM image and the corresponding FFT pattern of self-rolled-up Si nanomembrane (h).

2.2 Strain Analysis of Self-rolled-up Si Nanomembranes

We prepared self-rolled-up microtubes based on epitaxial Si nanomembranes with thicknesses of 6, 10, 20, and 30 nm, and the corresponding XRD characterizations are shown in Figure S10. **Figure 2a** shows a self-rolled-up microtube, and the Si microtube have no visible collapse and no significant gaps in the tube walls. More high-resolution scanning electron microscopy (SEM) images are used to count the radius distribution of Si microtubes (Figure S11). For 6, 10, 20, and 30 nm Si nanomembranes, the diameters of the microtubes are approximately 0.7, 1.1, 3.9, and 8.3 μm , respectively, as shown in Figure 2b. In order to analyze the relationship between the geometric characteristics and strain distribution of Si self-rolled-up nanomembranes, the thickness and initial strain must be considered. As the Si nanomembrane is a monolayer consisting of a single material, applying Nikishkov's multilayer nanomembrane rolled-up theoretical model formula may encounter insurmountable challenges.^[45] Here, an ideal strain model is proposed to solve the strain distribution problem in single-crystalline epitaxial monolayer nanomembranes. Si/Ge lattice mismatch is significantly reduced from the interface in the distance of several molecular layers. Thus, the single-layer epitaxial nanomembrane can be divided into two ideal layers, wherein layer A is near the epitaxial interface and has a higher strain and the other layer B is away from the epitaxial interface and has a lower strain. Then we define the following parameters: the thickness of Si nanomembrane and two ideal layers t_{Si} , t_A , t_B , initial strain of two ideal layers ε_A , ε_B , in which $t_{Si} = t_A + t_B$, $\Delta\varepsilon = \varepsilon_A - \varepsilon_B$. For pre-strained nanomembranes, the parametric solution of the nanomembrane radius after rolling is obtained by force and moment equilibrium equations:^[27,45]

$$R(t_A, \Delta\varepsilon) = \frac{t_A^4 + (t_{Si} - t_A)^4 + 2t_A(t_{Si} - t_A)(2t_A^2 + 2(t_{Si} - t_A)^2 + 3t_A(t_{Si} - t_A))}{6\eta t_A(t_{Si} - t_A)t_{Si}\Delta\varepsilon}$$

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Based on this equation, the corresponding results of t_A and $\Delta\varepsilon$ are obtained by traversing the parameters on ε_A , ε_B , and t_A in different total thicknesses of Si nanomembrane ($t_{Si} = 6/10/20/30$ nm), and collected from the intersection of the sets of solutions satisfying the corresponding diameters of self-rolled-up Si microtubes. As shown in Figure 2b, the calculated results of the relationship between the radius and thickness of Si nanomembrane are consistent with the experimental results. It is worth noting that the deposition of functional thin films (such as metallic electrodes) onto the epitaxial Si nanomembrane will alter the strain distribution within the epitaxial Si nanomembrane, ultimately resulting in a corresponding adjustment in the diameter of the Si microtube. The average strain in ideal layers A and B are exhibited in Figure 2c and Figure 2d respectively. Initial strains of layer B in Si nanomembranes of different thicknesses were 3.49% (I), 3.79% (II), 3.75% (III), and 3.71% (IV), respectively, as shown in Figure 2d. According to the corresponding experimental data points and minimum elastic energy theory, the tensile strains of both layer A and layer B exhibit varying degrees of decrease after release due to the requirements for force balance and moment balance of the two layers. This proposed ideal layer model is not only suitable for Si nanomembranes, but also for geometric prediction and strain analysis of various epitaxial single-crystalline nanomembranes.

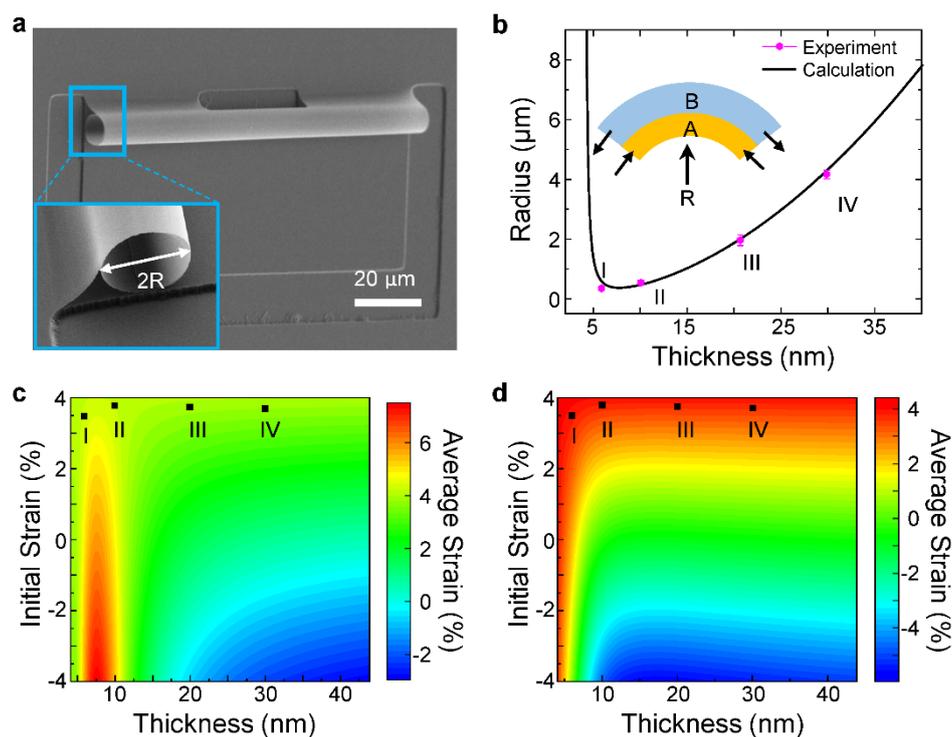


Figure 2. Thickness-dependent radius and corresponding strain distribution of Si nanomembrane microtube. a) SEM image of a self-rolled-up Si microtube. Inset: enlarged side view of the microtube. b) Calculation of the relationship between the radius of the Si microtube and the nanomembrane thickness. The experimental results of tuning the Si nanomembrane thickness are plotted on the curve (I, II, III and IV). The inset shows the formation mechanism for the strain-induced self-rolled-up of the epitaxial Si nanomembrane. Mappings of the bending-induced (c) compressive and (d) tensile strains related to the thickness of the Si layer with different initial strains. Spots I, II, III, and IV are the same samples as in panel b.

2.3 Electrical Properties of Si Microtubes

In addition to doping impurities, the electrical properties of ultrathin Si nanomembranes are distinctly affected by their surface/interface,^[5,46] such as lattice defects, surface dangling bonds, and organic residues. Generally, the surface of nanomembranes will be inevitably damaged during the physical or chemical release process. To minimize the damage to the surface of Si nanomembranes,

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we have designed a special process flow to prepare Si nanomembranes tubular optoelectronic devices, as shown in **Figure 3a** and Figure S12: i) Defining etching window by photolithography, and etching the epitaxial Si layer by reactive ion etching to expose Ge. ii) Defining electrodes by photolithography and depositing chromium (Cr)/gold (Au) (5 nm/5 nm) with an electron beam evaporation. iii) Depositing 30 nm of Al₂O₃ by atomic layer deposition. iv) Defining planar electrodes by photolithography and depositing Cr/Au (15 nm/50 nm). v) Using photolithography to define the pattern and etching the exposed Al₂O₃ layer with HF solution to expose the Si nanomembranes and the electrodes deposited in step ii). vi) Selectively etching the Ge layer in H₂O₂ solution to release Si nanomembrane with electrodes which rolls up into microtubes. During the subsequent self-rolled-up process, the thinner Cr/Au electrodes deposited on Si nanomembranes are always exposed to the outside of the Si microtubes and forms a van der Waals Au-Au contact with the planar electrodes. Once the Si nanomembrane is fully released, the Si wafer will be subsequently immersed in acetone. The microtubes are then isolated from the acetone via a critical point drier, as shown in Figure 3b. Since the Cr/Au electrode deposited by the electron beam evaporation cannot provide sufficient strain comparable to that in epitaxial Si, the diameters of the rolled-up Si microtubes with Cr/Au electrode are approximately 11 μm, which exceeds the diameter of the Si microtubes without electrode. Among the prepared microtube devices, we found that the yield of Si microtubes with two turns was higher, and their electrical properties are the most stable during electrical characteristics. Therefore, we selected Si microtubes with two turns as an example for further studying of carrier transport behaviors. As shown in Figure 3c, Si microtubes demonstrate linear and nonlinear current-voltage behaviors in the regions of low and high voltages, respectively. When redrawing the current-voltage curve of the device with logarithmic coordinates in Figure 3d, three ranges of different curvatures can be observed, indicating the existence of three transport mechanisms at different bias voltages. At 0~2 V, the slope of the Log(I)~Log(V) curve is 1.06 (Figure 3e), indicating that the device is in Ohmic contact and the current was driven by bias voltage.^[47,48] At 2~5 V, the slope of the curve Ln(I/V²) ~V⁻¹ is 1.84 (Figure 3f). The device was dominated by a tunneling mechanism, and a larger bias voltage can drive electrons through the potential barrier.^[49,50] At 5~10 V, the slope of the curve Ln(I/V²) ~V⁻¹ is -43.65 (Figure 3g), indicating that the device was still dominated by tunneling, but more hot electrons were generated under high bias voltage.^[51] Carriers tunneling through the barrier also results in thermal tunneling transport with

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thermal assistance. This could be caused by the excessive Joule heat generated by the tubular geometry structure under high bias voltage, which cannot be completely dissipated because the tubular Si microtube is suspended on the substrate without large-area contact with substrates, leading to a rapid increase in the temperature of the entire device, further affecting its electrical transmission.

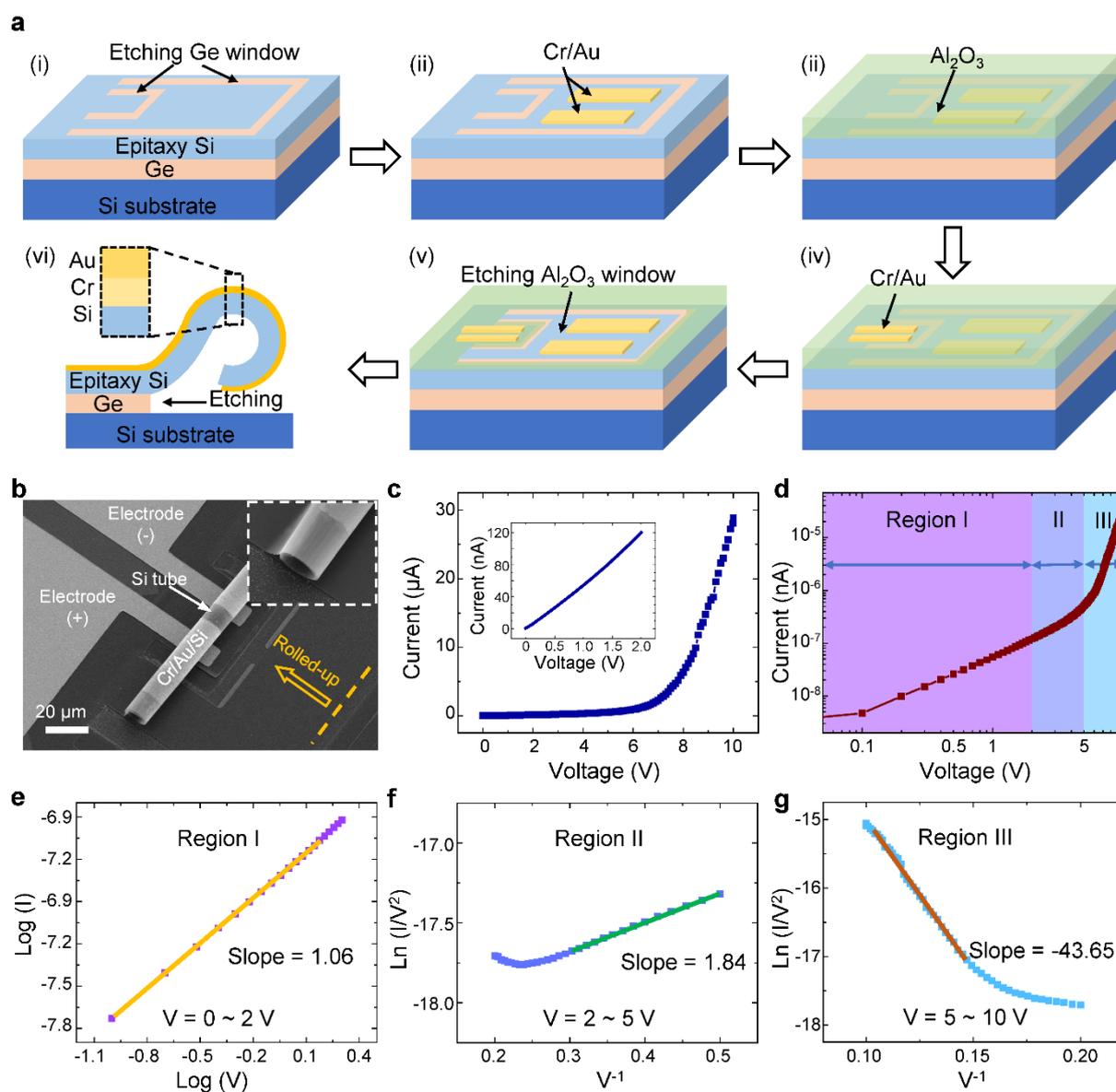


Figure 3. Device fabrication and charge transport mechanism of a tubular Si nanomembranes. a) Manufacturing steps for a self-rolled-up 3D tubular Si nanomembrane device. b) SEM image of self-

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rolled-up Si nanomembrane microtube with electrodes. The rolled-up electrodes are exposed on the outer surface of the microtube in contact with the planar electrodes. The inset shows a side view of self-rolled-up Si nanomembrane microtube. c) I-V curve of Si tube in a linear coordinate. The inset shows linearly correlated current-voltages at 0 to 1 V. d) I-V curve of Si microtube in logarithmic coordinates. Based on the slope of the current curve under different bias voltages, the transport state of the device can be roughly divided into three regions: I ($0\sim 2$ V), II ($2\sim 5$ V), and III ($5\sim 10$ V). e) The charge transport in region I is dominated by Ohmic law. f) The charge transport in region II is dominated by tunneling current. g) The charge transport in region III is dominated by thermally assisted tunneling current.

2.4 Photoresponse in VIS-NIRs of Si Microtube Photodetectors

We further investigated tubular photodetectors based on self-rolled-up Si nanomembranes for photodetection in the range of VIS-NIRs. The photocurrent mapping in **Figure 4a** confirms that the light-sensitive region of the Si nanomembrane microtube photodetector is the channel between the preset electrodes, indicating that the electrode layer and Si nanomembrane of our tubular device form a good Ohmic contact, and the device is a photoconductive photodetector. It is an important basis for achieving optical gain to improve photoconductor detection performance. In general, the bandgap of Si is 1.21 eV, and its cutoff wavelength is 1100 nm.^[52,53] The typical response peak for bulk Si usually falls within the range of 800-950 nm.^[54,55] But for thin Si nanomembranes, the response peak is usually shifted to a range of shorter wavelengths. We should mention that we ignore the radial changes in semiconductor properties of Si microtubes such as energy band and carrier mobility as the strain variation introduced by the self-rolling process is trivial in the case of ultrathin nanomembrane. Using a continuously tunable wavelength spectrometer, the spectral response of photodetectors from 400 to 1100 nm was tested, as shown in Figure 4b. The device exhibits a peak response of 700 nm with a response of 330 mA/W, which is comparable to commercial Si-based photodetector devices. For ultra-thin nanomembrane devices, enhanced performance can be attributed to the tubular geometry that traps incident light and promotes interaction between incident light and Si nanomembranes, thereby improving the light absorption of

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the device. Performance comparison of the Si tubular photodetectors and previous thin Si-based photodetectors is shown in Table S1. The dynamic response is measured under 520 nm incident light, and the corresponding photoresponse is shown in Figure S13. As shown in Figure 4c, the Si tubular photodetector have a fast photoresponse speed with a rise time of 370 μs and a decay time of 440 μs . Dislocations and impurities are generated on the surface of the released Si nanomembranes due to the chemical reaction between the Si nanomembranes and HF/H₂O₂,^[56-58] which reduces the response performance and prolongs the response time of the Si tubular photodetectors. In addition, the incident light has two characteristic directions that are respectively parallel and perpendicular to the tube axis for the tubular photodetector, as shown in Figure 4d. In the direction parallel to the microtube axis, its angle-dependent response of photodetector is fitted by a sinusoidal function, as shown in Figure 4e. This is consistent with conventional planar photoelectric devices. However, in the direction perpendicular to the axis of the microtube, our Si microtube photodetector demonstrates significant angle-independent photodetection. From the perpendicular direction, effective photodetection over $\pm 75^\circ$ can be achieved, and angle-independent photodetection can be achieved in the range of $\pm 60^\circ$, as shown in Figure 4f. We believe that the wide-angle detection characteristics of this tubular photodetector can provide new ideas for inspiring novel approaches in the design of photoelectric imaging systems.

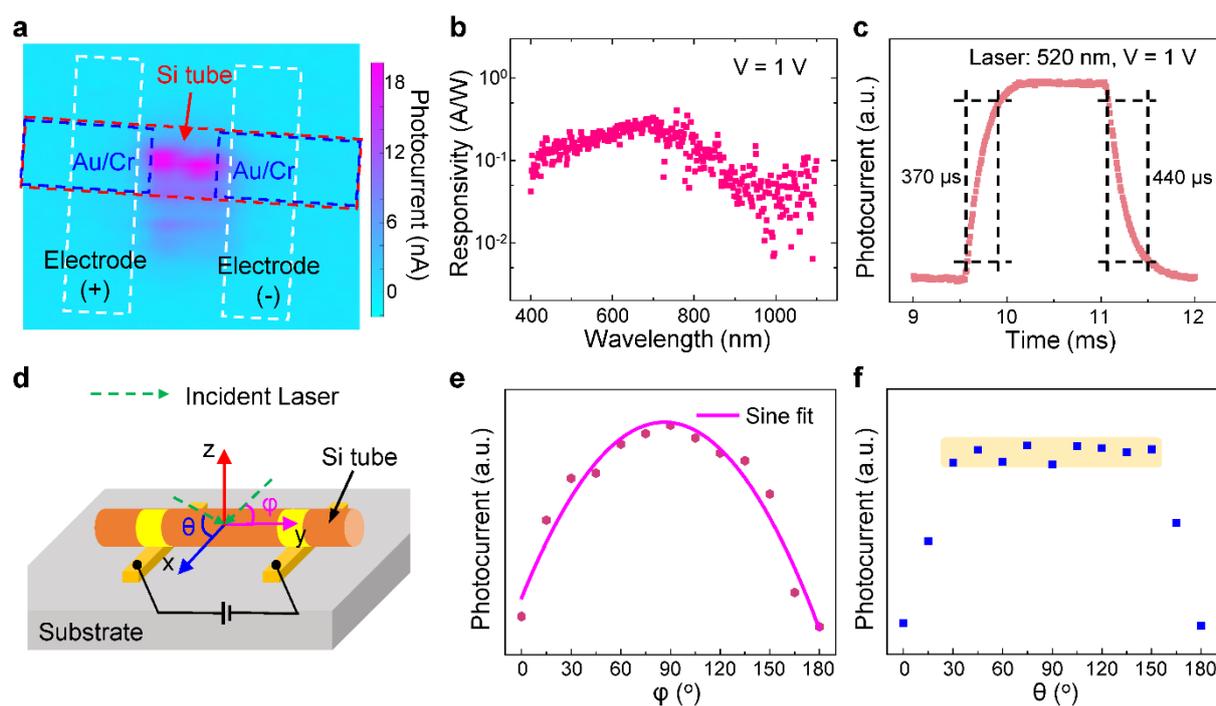


Figure 4. Photoresponse performance of Si microtubule photodetectors. a) The spatial photocurrent map obtained by laser scanning (wavelength 520 nm, power 15 μ W, spot size 1 μ m) over the sample with 1 V bias voltage. The photocurrent occurs exactly at the bare Si microtube, which is marked by a blue dashed line. b) Photoresponsivity of the Si microtube photodetector for incident light wavelengths ranging from 400 to 1100 nm at $V = 1$ V. c) 90-10% rise and decay time of photocurrent are measured as 370 and 440 μ s, respectively. d) Schematic diagram of the setup of angle-dependent photocurrent test. The bright green arrow represents the direction of the incident laser. In xyz coordinates, the coordinate origin is at the symmetrical center point of the Si microtube, the x-axis (y-axis) is perpendicular (parallel) to the tube axis, and the z-axis is perpendicular to the substrate. In the xz (yz) plane, the laser is incident at an angle of θ (ϕ) to the x-axis (y-axis). e) The photocurrent curve is dependent on the laser incident angle ϕ and fitted with the sin function. f) The photocurrent curve as a function of the laser incident angle θ . The responsivity in 30° to 150° highlighted in yellow has no angle dependence.

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The geometric structure of the tubular Si nanomembrane device can not only enhance the photoresponse, but also achieve varying trap effect on linearly polarized light. In the experiment and simulation, the laser was incident vertically from the top of the Si nanomembrane microtubes, and the polarization state of the laser was changed by rotating the half-wave plate or tuning the software setting, respectively. A schematic of the polarized light current test setup is shown in **Figure 5a**. Rotating the half-wave plate alters the polarization angle of the linearly polarized light, while maintains the stable laser power. When the rotation angle of the half-wave plate is $\alpha/2$, the polarization of the linearly polarized light rotates at an angle of α . First, we simulated the electric field distribution of 940 nm polarized light at different polarization angles in the Si nanomembrane microtube structure by using FDTD software, as shown in Figure 5b-h. Accordingly, under the irradiation of 520 nm laser, the simulated electric field distribution is shown in Figure S14. As the polarization angle α changes from 0° to 90° , the electric vector of linearly polarized light gradually rotates from parallel to perpendicular to the tube axis. The intensity of the electric field in the corresponding microtubes decreases significantly, indicating that the coupling effect of tubular structures on linearly polarized light in different directions varies. Limitation of the polarized light at varying angles presents an advantage for the detection of polarization in Si nanomembrane devices. The observed polarization anisotropy is well explained by classical electromagnetic theory (Note S1). Significant polarization sensitivity is attributed to the substantial dielectric contrast between the microtube wall and its surrounding air environment. We quantitatively simulate this effect by treating Si microtubes as infinite dielectric ring cylinders in a vacuum. When the incident field is parallel to the axial polarization of the microtube, the electric field inside the ring cylinder enhanced compared with the polarization that is perpendicular to the microtube axis. To investigate this quantitatively, the photocurrent at the most sensitive polarization angle is divided by the photocurrent at the least sensitive polarization angle to get dichroic ratios. Figure 5i, j, and k show that the dichroic ratios of the device for 940, 830, and 520 nm laser polarization detection are 1.24, 1.16, and 1.14, respectively, which are comparable to those of most non-lens-assisted polarization photodetectors. More, we compared the performance of polarization detection photodetectors realized by the geometrical structure in recent years. Our Si microtubular photodetectors have good performance in response rate and response time (Figure S15).

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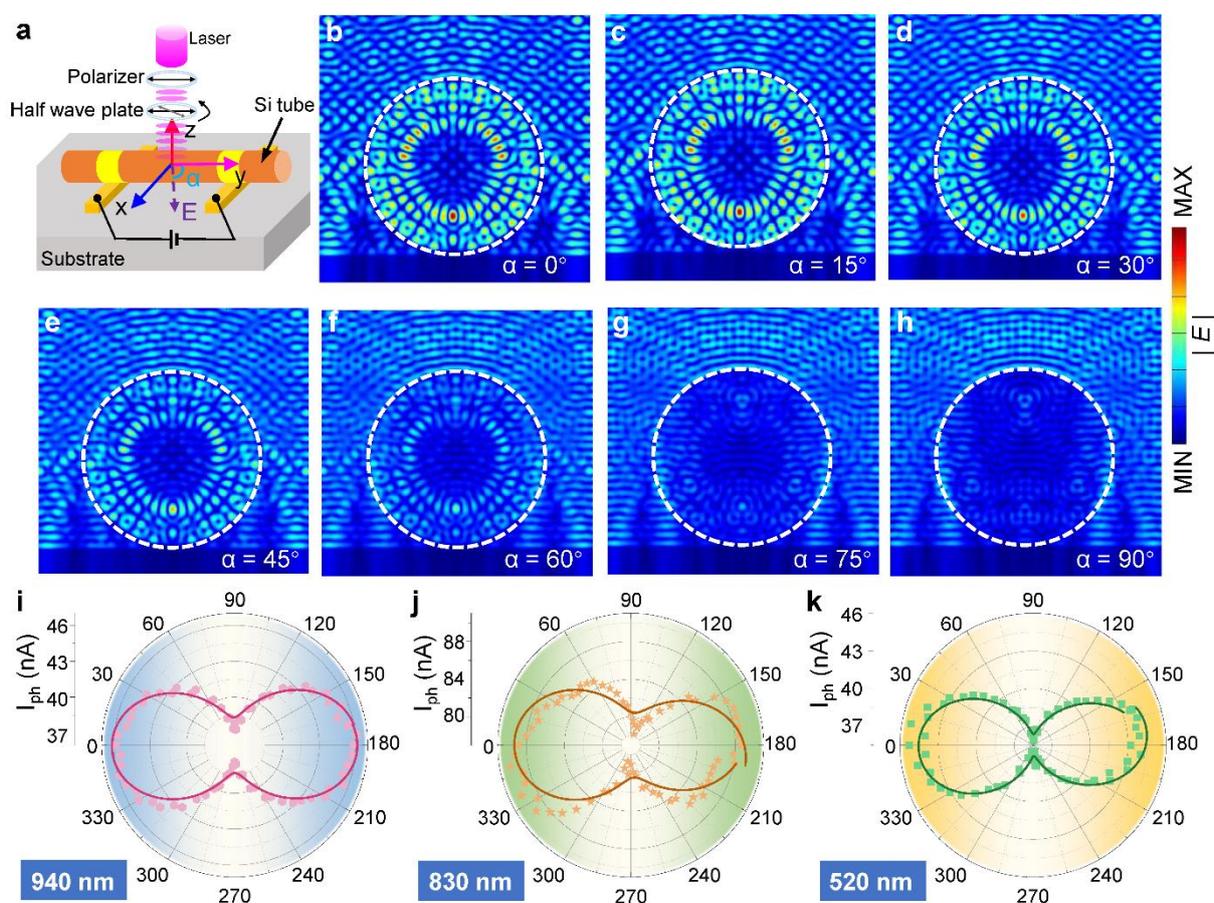


Figure 5. Polarization detection characteristics of self-rolled-up Si microtubular photodetector. a) Schematic of polarization detection test setup. The incident laser is modulated by a polarizer and a half-wave plate and irradiates perpendicularly to the device. The y-axis is oriented parallel to the tube axis, while the x-axis is perpendicular to it. The angle between the direction of the electric vector of linearly polarized light and the y-axis is defined as α . (b-h) Simulated electric field distribution in Si microtube illuminated by linearly polarized light (940 nm) for $\alpha = 0^\circ$ – 90° with a step of 15° . The Purple circles represent the cross section of self-rolled-up Si nanomembranes. Measured photocurrents as a function of polarizer angle α under linearly polarized illuminations at 940 (i), 830 (j), and 520 nm (k) from self-rolled-up Si microtube photodetector.

3. Conclusion

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In summary, we obtained high-quality, freestanding single-crystalline Si nanomembranes by wet etching the Ge sacrificial layer. The release of residual strain introduced by lattice mismatch at the Si-Ge epitaxial interface drives Si nanomembranes to self-assemble into microtube structures. The thickness-dependent self-rolled-up microtube radius and strain distribution of nanomembranes were analyzed experimentally and theoretically. Despite the release process, the self-rolled-up Si nanomembrane still maintains good electrical conduction characteristics and sensitive VIS-NIRs detection performance, especially in the fields of wide-angle optical coupling detection and polarization-sensitive detection. In addition, our self-rolled-up epitaxial single-crystalline nanomembrane microtube strategy demonstrates the potential to establish novel avenues to produce Si, Ge, and other materials-based advanced electronic and optoelectronic devices, aiming at achieving next-generation optoelectronic integrated chips featuring novel functionalities.

4. Materials and methods

Fabrication of Si microtubes: First, 40 nm Ge layer was epitaxially grown on a Si wafer, and then Si layers with thicknesses of 6, 10, 20, or 30 nm was epitaxially grown on Ge. The instrument used was solid-source molecular beam epitaxy (MBE) and the growth temperature was 280 °C. The concentration of phosphorus dopant in epitaxial Si is $\sim 4 \times 10^{17} \text{ cm}^{-3}$. The epitaxial Si was initially cleaned using hydrofluoric acid (HF) acid to remove the native oxide layer from the surface. A layer of photoresist with about 2 μm thickness was spin-coated and etching windows were defined by photolithography. After development, the window was exposed and etched by reactive ion etching for 40 s (35 sccm SF_6 and 18 sccm CHF_3 flow, 30 mTorr chamber pressure, and 50 W etching power). The photoresist layer was then removed by ultrasonication in acetone for 120 s. Cr/Au electrodes with different thicknesses were deposited by e-beam evaporation. In the microtube fabrication process with electrodes, 30 nm Al_2O_3 was deposited with ALD to isolate electrical conduction between the planar metal electrode and the substrate. For the rolling process, patterned Si nanomembranes with/without electrodes were released from the substrate by using 30% H_2O_2 solution at 70°C for 15 min. Finally, critical point dryer (CPD) was applied to dry the self-rolled-up Si microtubes, avoiding structural collapse.

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Materials and structure characterization of Si nanomembranes: A Lab Ram HR800 from HORIBA spectrometer and an Olympus $\times 100$ objective lens was used for Raman measurements. The structure of self-rolled-up Si nanomembranes was characterized via SEM. Before the characterization by TEM, the sample lamella was cut by FIB, and deposition of Pt layer was used to protect the surface of the Si nanomembrane during the preparation process. TEM and STEM images were collected on JEOL JEM-ARM200F at 200 kV.

Electrical and optoelectronic characterization of Si microtubes: The electrical properties of Si nanomembranes were performed on a Keysight B2902B. The VIS-NIRs photoresponse measurement was under Xenon lamps with a spot diameter of ~ 100 μm to ensure uniform power on the device. All other optoelectronic measurements were taken by an MStarter 200 optoelectronic measurement system from Maita Optoelectronic Technology Co., LTD.

Optical simulation: Simulation of electrical field distribution in microtube Si nanomembranes was realized by commercial software Lumerical FDTD solutions. The model consists of a Si tubular structure (outer diameter $D_{outer} = 10$ μm , inner diameter $D_{inner} = 9.94$ μm , length $L = 40$ μm) on epitaxy-Si/Ge/Si sandwich structure ($t_{\text{epitaxy-Si}} = 30$ nm, $t_{\text{Ge}} = 40$ nm, $t_{\text{SiSub}} = 100$ μm , $L = 40$ μm) to simulate the tubular Si nanomembrane on substrate. The boundary condition of the direction perpendicular to the substrate is set to perfect match layer, while the periodical boundary condition is applied in the direction parallel to the substrate due to the periodicity of the Si microtube array. Plane wave ($\lambda = 940$ nm and 520 nm, spot radius $r = 1$ mm) is utilized to simulate an incident polarized light source.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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Author Contributions

B. Wu, and Z. Zhang contributed equally to this work. Y. Mei conceived the idea for the project and designed the experiments. B. Wu, C. You, and Z. Zhang fabricated the devices and performed the electronic and optoelectronic measurements. Z. Zhang, T. Cai and Y. Wang performed optical simulation and calculation. B. Wu, Z. Zheng, and X. Li performed the STEM experiments and analyzed the data. J. Wang, H. Li and B. Wu performed the Raman experiments. B. Wu, G. Huang and Y. Mei cowrote the manuscript. All authors discussed the results and commented on the manuscript.

Conflict of Interest

The authors declare no conflict of interest.

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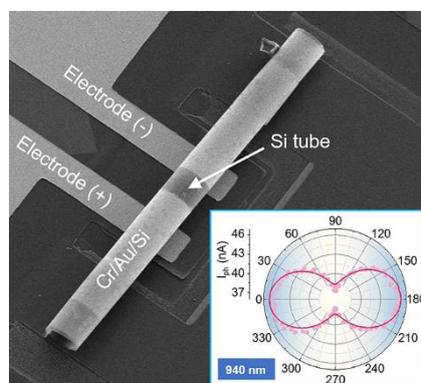
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Tubular microtubes of single-crystalline Si nanomembranes for photodetectors are prepared by a releasing and rolling process. The tubular geometry can trap light to improve the photoresponsivity of ultra-thin Si nanomembranes, and demonstrates the advantage of wide-angle light coupling. Furthermore, the Si microtubes exhibit obvious polarization angle-dependent light absorption, enabling polarization-sensitive detection in the range of visible to near-infrared.

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Self-rolled-up ultrathin single-crystalline silicon nanomembranes for on-chip tubular polarization photodetectors



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